

Form PTO 1449  
(Modified)

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LIS REFERENCE CITED BY APPLICANT

U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICEATTY DOCKET NO.  
259189US40PCTSERIAL NO.  
10/509,899APPLICANT  
Motoo ASAI, et al.FILING DATE  
June 29, 2005GROUP  
2633 2814

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
BJ	AA	6,788,874 B1	9/7/2004	Shinji ISHIKAWA, et al.			
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
	AM						
	AN						

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION	
					YES	NO
BJ	AO	2000-321442	11/24/2000	Japan (with English Abstract, corresponding US 6,788,874 B1)		X
BJ	AP	2000-66051	3/3/2000	Japan (with English Abstract and Unedited Computer-Generated English Translation)	X	
BJ	AQ	2000-199827	7/18/2000	Japan (with English Abstract and Unedited Computer-Generated English Translation)	X	
	AR					
	AS					
	AT					
	AU					
	AV					

## OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)

BJ	AW	Ray T. CHEN, et al., "Fully Embedded Board-Level Guided-Wave Optoelectronic Interconnects", PROCEEDINGS OF THE IEEE, vol. 88, no. 6, June 2000, pages 780-793	
	AX		
	AY		
	AZ		
Examiner	Bilkis Jahan	<input type="checkbox"/> Additional References sheet(s) attached	
		Date Considered	04/14/10

\*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.